INDIAN INSTITUTE OF TECHNOLOGY, ROORKEE DEPARTMENT OF ELECTRICAL ENGINEERING

Dated: 1/07/2025

ADVERTISEMENT TO FILL UP PROJECT POSITIONS*

Applications are invited from Indian nationals only for project position(s) as per the details given below for the research project under the Principal investigators: **Dr. Satish Shamsundar Belkhode**, **Dr. Bhavesh Kumar Bhalja**, and **Dr. Siba Kumar Patro**, **Department of Electrical Engineering**, **Indian Institute of Technology Roorkee**.

- 1. Title of project: Development of wide-bandgap semiconductors based highly efficient power electronics systems for Electric Vehicle
- 2. Sponsor of the project: Anusandhan National Research Foundation (ANRF), Govt. of India.
- 3. Project position(s) and number:
 - a. Junior Research Fellow (JRF) and 4 number
 - b. Research Associate-I (RA) and 2 number
 - 4. Qualifications: Candidates that fulfil following qualification criteria are encouraged to apply for the position:
 - A. Minimum of 4-year Bachelor's degree OR a Master's degree
 - B. Qualifying Degree Disciplines: Electrical or Electrical and Electronics Engineering or Equivalent disciplines.
 - C. Qualified in a national level test, such as- GATE / CEED / JEST / UGC-NET /CSIR-NET including lectureship (Assistant Professorship) / Ph.D. only. (Requirements of the national level tests such as GATE/NET is exempted for candidates having ≥ 8.0 CGPA in qualifying degree from the Ministry of Education funded Technical Institutions (erstwhile CFTIs of MoE))
- 5. Emoluments:
 - a. JRF: Rs. 37,000/- p.m. (for first two years) SRF: Rs. 42,000/- p.m. (for third year)
 - b. RA: Rs. 58,000/- p.m.
- 6. Duration: 3 years (The selected JRF candidate will have the opportunity to register for the Ph.D. program at IIT Roorkee as per institute norms)
- 7. Job description:
 - A. Design and Development of High-Power Bi-directional EV Charger for Vehicle-to-Grid (V2G), Vehicle-to-Vehicle (V2V), and Vehicle-to-Home (V2H) Integration in a Scalable 120 kW System
 - B. Design of new Synchronous reluctance motor for EV applications, development and validation of hardware prototype of DC-DC and AC-DC converter to drive SynRM

C. Terms:

- a. Candidates before appearing for the interview shall ensure that they are eligible for the position they intend to apply. Qualifications for the position are mentioned in point 4.
- b. Preference will be given to OBC/SC/ST candidates on equal qualifications and experience.
- c. Candidates should also make themselves familiar with job description (Point 7) beforehand.
- d. Candidates desiring to appear for the Interview should submit their applications through the online form for which link and QR code is given below. Candidates will require following documents for filling the online form: i) Detailed CV including chronological discipline of degree/certificates obtained, papers published, research experience, etc. ii) Copies of degree/certificate and experience certificate.

Form link: https://forms.gle/9rUA3hsQgNQngVpV7

Or Scan the QR code:





- e. The last date for application submission is 10th July 2025 by 5 PM
- f. The interview will be held online. Date and time of interview along with meeting link will be communicated to the shortlisted candidates via email.
- g. The project position starts at the earliest possible, preferably by end of July.

Dr. Satish Belkhode,

Email: satish.belkhode@ee.iitr.ac.in

Tel: +91 1332 285843

Dr. Bhavesh Kumar Bhalja,

Email: bhavesh.bhalja@ee.iitr.ac.in

Tel: +91 1332 284804

Dr. Siba Kumar Patro,

Email: skpatro@ee.iitr.ac.in

Tel: +91 1332 285145

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